

General description

WS2916B-1313-4P is the new generation of digital LED from WORLDSEMI, specially developed for high-definition image display applications. Each channel can provide up to 16-bit grayscale data, making it highly suitable for large image display.

Highly integrated digital leds do not require any electronic components, including capacitors, externally.

The data transfer protocol use Single-wire RZ protocol. After the pixel is powered on and reset, the DIN accepts the data transmitted from the controller. The first 48-bit data sent over is extracted by the first pixel and then sent to the data latch inside the pixel. The remaining data, after being shaped and amplified by the internal shaping processing circuit, starts to be forwarded and output to the next cascaded pixel point through the DO port. With each pixel point of transmission, the signal is reduced by 48 bits. The pixel points adopt automatic shaping and forwarding technology, which makes the number of cascaded pixel points not limited by signal transmission, but only restricted by the signal transmission speed requirement.

Main Features

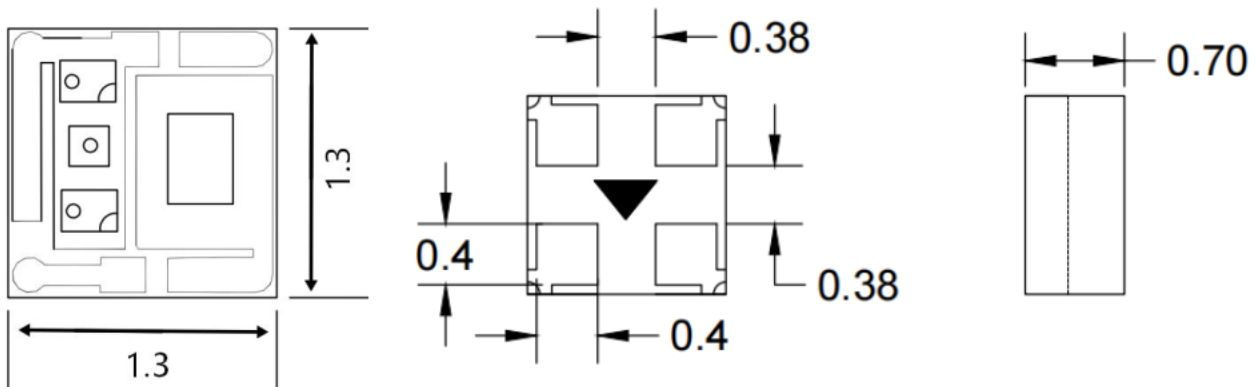
- 0.6uA static current, ultra-low static power consumption, ultra-low voltage constant current.
- The control integrated circuit and the LED share the only power source.
- Control circuit and RGB chip are integrated in a package of **1313** components, to form a complete addressable pixel.
- It is equipped with an internal signal shaping circuit. After any pixel receives the signal, it undergoes waveform shaping before being output, ensuring that the waveform distortion of the line does not accumulate.
- It is equipped with built-in power-on reset and power-off reset circuits.
- OUT R / G / B output gray level: R,G,B 65536 gray scale.
- RGB Port with 4KHz refresh frequency
- ●Special pin treatment makes it easier to solder.
- The serial cascade port can receive and decode data through a single signal line.
- The internal IC is highly integrated and features reverse connection protection.
- 1.3mm*1.3mm*0.65mm (thickness), ultra-small and ultra-thin size.

Main Applications

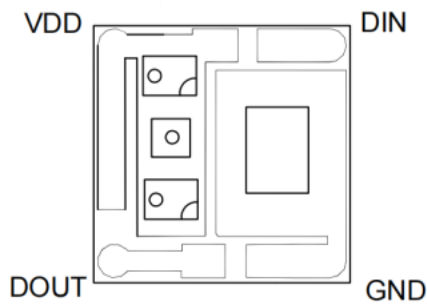
- LED transparent screen, LED pixel screen, LED special-shaped screen, various electronic products.

Mechanical Dimensions (Unit:mm)

Tolerance: $\pm 0.15\text{mm}$



PIN Configuration



PIN Function

NO.	Symbol	PIN	Function description
1	VDD	POWER SUPPLY	Power supply
2	DOUT	DATA OUT	Control data signal output
3	VSS	GROUND	Ground,data & power grounding
4	DIN	DATAINPUT	Control data signal input

Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$, $V_{SS}=0\text{V}$)

Parameter	Symbol	Ratings	Unit
Power supply voltage	V_{DD}	+3.3~+5.5	V
Logical Input Voltage	V_I	-0.3V ~ $V_{DD}+0.7$	V
Working temperature	T_{opt}	-40 ~ +85	$^{\circ}\text{C}$
Storage temperature(bulk material)	T_{stg}	-40~+105	$^{\circ}\text{C}$
Storage temperature(Whole reel)	T_{stg}	-40~+70	$^{\circ}\text{C}$

Electrical Characteristics ($T_A=25^{\circ}\text{C}$, $V_{DD}=5\text{V}$, $V_{SS}=0\text{V}$)

Parameter	Symbol	Min	Typ	Max	Unit	Conditions
Quiescent current	I_{VDD}	—	0.6	1	μA	No signal transmission&RGB channel is turned off
Port output current	I_{out}	—	9	—	mA	OUTR+OUTG+OUTB
Input Current	I_I	—	—	± 1	μA	$V_I=V_{DD}/V_{SS}$
High-level input voltage	V_{IH}	$0.55V_{DD}$	—	—	V	D_{IN} , SET
Low-level input voltage	V_{IL}	-0.3	—	$0.3V_{DD}$	V	D_{IN} , SET
Hysteresis voltage	V_H	—	0.35	—	V	

Switching Characteristics ($T_A=25^{\circ}\text{C}$, $V_{DD}=5\text{V}$, $V_{SS}=0\text{V}$)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Condition (Working current)
Transmission delay time	t_{PLZ}	—	—	300	ns	$CL=15\text{pF}$, $D_{IN} \rightarrow D_{OUT}$, $R_L=10\text{K}\Omega$
Fall time	t_{THZ}	—	—	120	μs	$CL=300\text{pF}$, OUTR/OUTG/OUTB
Input capacity	C_I	—	—	15	pF	—

LED Characteristics

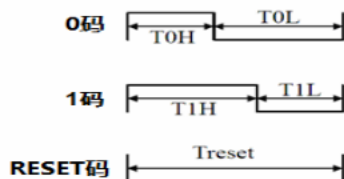
Parameter	Symbol	Color	Test Condition: VDD=5V			
			Min.	Typ.	Max.	Unit
Luminous intensity	IV	Red	265	—	400	mcd
		Green	635	—	930	
		Blue	120	—	200	
Wavelength	λ_d	Red	618	—	628	nm
		Green	513	—	523	
		Blue	465	—	475	
Luminous Angle	$\Theta_{1/2}$	/	-	120	-	Deg

Data Transfer Time

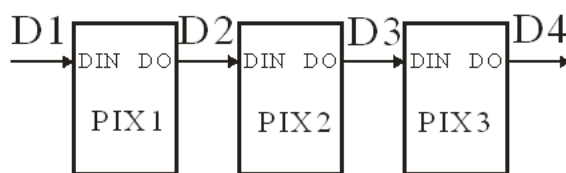
T0H	0 code, high voltage time	200ns~320ns
T1H	1 code, high voltage time	520ns~800ns
T0L	0 code, low voltage time	800ns~1.2 μ s
T1L	1 code, low voltage time	480ns~1 μ s
RES	Frame unit, low voltage time	>280 μ s
Data Cycle: T0H+T0L $\geq$ 1.25 μ s; T1H+T1L $\geq$ 1.25 μ s		

Timing waveform diagram

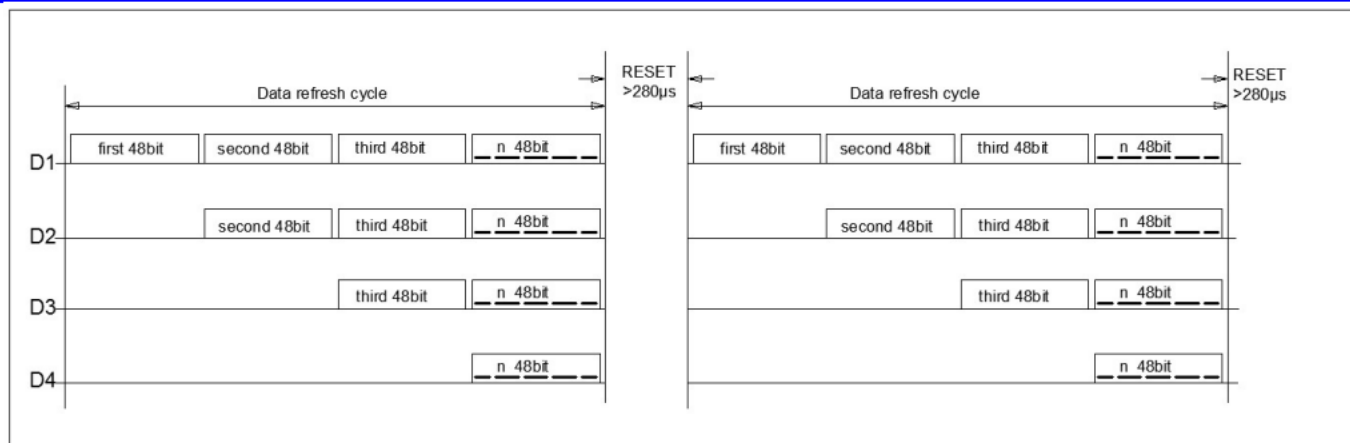
Sequence Chart



Cascade Method:



Data Transmission Method



Note: The data of D1 is send by MCU, and D2, D3, D4 through pixel internal reshaping amplification to transmit.

Data structure

- 16-bit current gain data structure, high-bit first transmission, sent in GRB sequence

IG4	IG3	IG2	IG1	IG0	IR4	IR3	IR2	IR1	IR0	IB4	IB3	IB2	IB1	IB0	校验码
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Note: The check code can be either 0 or 1

- 48-bit RGB grayscale data structure, high-bit first transmission, sent in GRB sequence

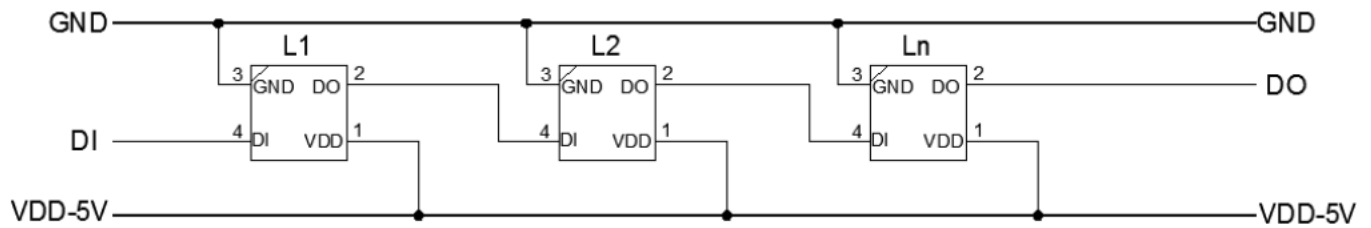
Composition of 48bit Data

G15	G14	G13	G12	G11	G10	G9	G8	G7	G6	G5	G4	G3	G2	G1	G0	...Continued...		
...Continued...		R15	R14	R13	R12	R11	R10	R9	R8	R7	R6	R5	R4	R3	R2	R1	R0	...Continued...
...Continued...		B15	B14	B13	B12	B11	B10	B9	B8	B7	B6	B5	B4	B3	B2	B1	B0	

Current gain

Current gain setting	RGB Current (mA)
0	0
1	0.28
2	0.56
3	0.84
4	1.16
5	1.45
6	1.72
7	2.01
8	2.36
9	2.65
A	2.92
B	3.20
C	3.51
D	3.80
E	4.07
F	4.34
10	4.79
11	5.07
12	5.34
13	5.61
14	5.92
15	6.20
16	6.47
17	6.74
18	7.08
19	7.36
1A	7.62
1B	7.89
1C	8.20
1D	8.48
1E	8.73
1F	9.00

Typical Application Circuit:



Depending on the power quality, a filter capacitor can be appropriately added in parallel between GND and VDD (it is recommended to take a value of 100nf).

Remark:

1. DI is the main input signal and BI is the auxiliary input signal. Under normal circumstances, the LED extracts signals from the DI. When one of the middle leds is damaged, the latter LED extracts the signal from the BI without affecting the signal transmission of the subsequent leds.
2. For finished product application, the BI pin of the first LED should not be left floating. It is recommended to connect it to GND.

WORLDSEMI TOP SMD LED Using Instructions

1. Summary

To make the best use of WORLDSEMI's LED, please refer to the below precautions, they are of same usage method as other electronic components.

2. Cautions

2.1. Dust & Cleaning

The surface of the LED is encapsulated with modified epoxy resin because it plays a very good role in protecting the optical performance and aging resistance. The modified epoxy resin is easy to stick with dust and must be kept clean. When there's a certain amount of dust on the surface of the LED, it won't affect brightness, but dust proof should be taken care of. Promoting the use of unsealed package in preference to others and the assembled LEDs should be placed in a clean container.

Avoid using the organic solvents to clean the dust on the LED surface and it's necessary to confirm whether the cleaning fluid will dissolve the LED.

Do not clean the LEDs by the ultrasonic. Some parameters affecting the LED performance must be evaluated if have no alternative but to the ultrasonic cleaning method, such as ultrasonic power, baking time and assembly conditions, etc.

2.2. Moisture-proof packaging

TOP SMD LEDs are moisture sensitive components. LEDs are packaged in aluminum foil bag to prevent the from absorbing moisture during transport and storage. A desiccant is placed in the bags to absorb moisture. If the LED absorbs moisture, then it evaporates and expands when in reflow process, which may break the colloid from the bracket and damage the optical performance of LED. For this reason, moisture-proof packaging is to prevent the from absorbing moisture during transport and storage. The moisture resistance rating of WORLDSEMI's LED is: **LEVEL 5a**.

Tabel I - IPC/JEDEC J-STD-020 Moisture/Reflow Sensitivity Classification

MSL Level	Workshop Life	
	Time	Conditions
LEVEL1	Unlimited	≤30°C/85%RH
LEVEL2	1 Year	≤30°C/60%RH
LEVEL2a	4 Weeks	≤30°C/60%RH
LEVEL3	168 Hours	≤30°C/60%RH
LEVEL4	72 Hours	≤30°C/60%RH
LEVEL5	48 Hours	≤30°C/60%RH
LEVEL5a	24 Hours	≤30°C/60%RH
LEVEL6	Take-out and Use immediately	≤30°C/60%RH

2.3 SMT Instruction:

2.3.1 It is recommended that opening the Vacuum plastic bag before SMT, and put the whole reel into the oven for dehumidification and drying (**Bake at 70 ~ 75°C $\cong$ 24H**);

2.3.2 From the led taken out of the oven to the completion of high temperature welding (including multiple reflow welding, tin immersion, wave soldering, heating maintenance and other high temperature operations/operations), the time period shall be controlled **within 24Hours** (Under condition of **T<30°C, RH<60%**);

2.3.3 After the LED paste is printed on the PCBA, SMT process should be completed as soon as possible, **it is recommended not to exceed 1H**;

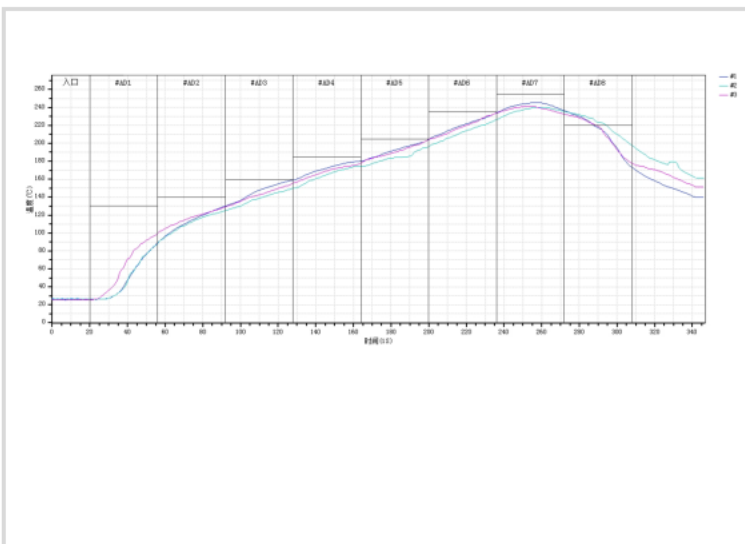
2.3.4 Bulk material LED, such as production surplus, machine material, maintenance material, can not be used directly if exposed to the air for a long time. It is recommended to be dehumidified and dried before being used.

Whole reel baking: 70 ~ 75°C* $\cong$ 24H or bulk led baking: 120°C*4H.

3. SMT Reflow Soldering

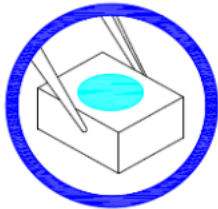
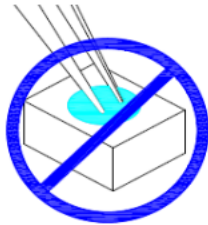
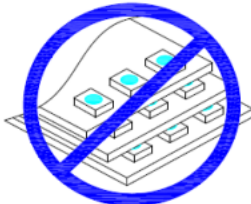
Refer to the parameters listed below, the experimental results prove that the TOP SMD LED meets the JEDEC J-STD-020C standards. As a general guideline, it is recommended to follow the SMT reflow temperature curve recommended by the solder paste manufacturer.

Curve Description	Lead-free
30°C ~ 150°C preheating slope	1~4 °C/s
30°C ~ 150°C preheating time	60~120 s
150°C to 200°C constant temperature slope	0~3 °C/s
150°C ~ 200°C constant temperature time	60~120 s
LIQUID REGION temperature (TL)	217°C
Peak Temperature (Tp)	245°C
Reflow slopeTime (tp)	0~3 °C/s
Reflow soldering time	45~90 s
Cooling Rate	-4~0 °C/s
Room Temperature to Peak Holding Time	<6 min



Remarks: 1. All the above temperatures refer to the temperatures measured on the surface of the package body

4. Assembly Precautions

1. Clip the LED from its side.	2. Neither directly touch the gel surface with the hand or sharp instrument, it may damage its internal circuit.	3. Not to be double stacked, it may damage its internal circuit.	4. Can not be stored in or applied in the acidic sites of PH<7.
			

Modify Record

Version №	Status Bar	Modify Content Summary	Date	Reviser	Approved
V1.0	N	New	20260822	Chen YongZhao	Yin HuaPing

Remarks:

1. Version number plus "0.1" if for add & modify parameters, eg. V1.0 → V1.1
2. Major revision or many parameters modified, version number plus "1.0", eg. V1.0 → V2.0
3. No version number is attached to Part Number

Initial version: V1.0, Status bar: N--New, A--Add, M--Modify, D--Delete.